

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): K.L. Davison et al.
Case: 4-7-23
Serial No.: To Be Assigned
Filing Date: November 26, 2003
Group: To Be Assigned
Examiner: To Be Assigned

Title: Methods and Apparatus for Integrated Circuit Device
Power Distribution Via Internal Wire Bonds

INFORMATION DISCLOSURE STATEMENT

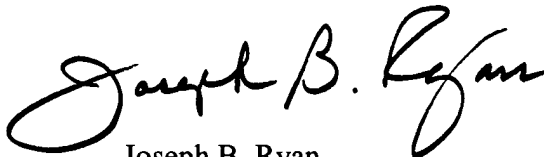
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying Form PTO-1449: U.S. Patent No. 5,751,065 issued on 05/12/98 to Chittipeddi et al.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



Date: November 26, 2003

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FORM PTO-1449 (MODIFIED)

Applicant(s): K.L. Davison et al.
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Serial No.: TBA
Filing Date: November 26, 2003
Group: TBA

**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

U.S. PATENT DOCUMENTS

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE
_____	1. 5,751,065	05/12/98	Chittipeddi et al.		

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO

OTHER DOCUMENTS

EXAMINER		
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.